

Efficient Sensitivity Analysis of Lossy Multiconductor Transmission Lines With Nonlinear Terminations

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Abstract—An efficient approach for sensitivity analysis of lossy multiconductor transmission lines in the presence of nonlinear terminations is described. Sensitivity information is extracted using the recently developed closed-form matrix-rational approximation of distributed transmission-line model. The method enables sensitivity analysis of interconnect structures with respect to both electrical and physical parameters. An important advantage of the proposed approach is that the derivatives of the modified nodal admittance matrices with respect to per-unit-length parameters are obtained analytically.

Index Terms—Interconnects, optimization, sensitivity analysis, transmission lines.

I. INTRODUCTION

THE ever-increasing quest for higher operating speeds, miniature devices, and denser layouts has made signal integrity analysis a challenging task. As signal frequencies approach the gigahertz range, interconnects behave like distributed transmission lines and effects such as signal delay, crosstalk, ringing, and distortion become prominent. This phenomenon can be observed at many levels of hierarchy, such as on-chip, packaging, multichip modules (MCMs), and printed circuit boards (PCBs). Consequently, improperly designed interconnects can result in poor performance, reliability and may cause false switching [1]–[4].

Interconnects play an important role in determining the circuit's density, power consumption, and clock frequency. For example, increasing the circuit's density leads to shorter interconnects which reduce the problem of delay and reflections. However, this leads to greater crosstalk between adjacent interconnects. Designers must make proper tradeoffs, often between conflicting design requirements, to obtain the best possible performance. Hence, efficient and accurate sensitivity information with respect to interconnect parameters are required by optimizers to solve large nonlinear circuits with embedded distributed subnetworks [5]–[7].

However, for large nonlinear networks with distributed interconnects, the task of finding the best possible design becomes too complicated due to the following difficulties.

- 1) The first major difficulty is due to the mixed frequency/time problem, which is encountered while linking distributed transmission lines with nonlinear circuit simulators. This is because, distributed transmission lines are described by partial differential equations which are best solved in the frequency domain, whereas nonlinear elements are described only in the time domain (nonlinear

ordinary differential equations). Hence, it is essential to model interconnects such that they can be directly included in a nonlinear circuit simulation environment.

- 2) The second difficulty is due to the excessive CPU time associated with the simulation of interconnect networks.

A significant amount of research has been done to address the simulation of distributed interconnects in the presence of nonlinear elements [1]–[17]. Approaches based on conventional lumped segmentation provide a brute force solution to the mixed frequency/time problem [8]. However, this leads to large circuit matrices, rendering the simulation inefficient. Also, there exist other algorithms based on the method of characteristics [9], [10], optimization techniques [11], Chebyshev polynomials [7], [12], and integral congruent transformations [13]. Recently, an efficient multiconductor transmission-line (MTL) model based on a closed-form Padé approximation has been proposed [14]–[16]. The advantages of this algorithm are as follows: the macromodel of the transmission-line equations is obtained analytically (in terms of predetermined constants and the per-unit-length parameters), the passivity of the macromodel is guaranteed, the method can be easily incorporated with passive model-reduction algorithms [16], [17], and the method can handle transmission lines with lossy as well as frequency-dependent parameters.

In this paper, a new method to perform sensitivity analysis of nonlinear circuits with distributed transmission lines is presented. The method uses a matrix-rational approximation-based algorithm to model distributed interconnects as well as to derive the network sensitivity with respect to any interconnect parameter. A major advantage of the proposed method is that the derivatives of the circuit matrices are obtained analytically, in terms of predetermined constants and per-unit-length parameters, facilitating efficient formulation of the sensitivity network.

The rest of the paper is organized as follows. In Section II, a brief review of formulation of circuit equations is given. In Section III, we outline the steps involved in the sensitivity analysis. Section IV presents the proposed algorithm for sensitivity analysis of nonlinear networks with distributed components. In Sections V and VI, we present the computational results and conclusions, respectively.

II. FORMULATION OF CIRCUIT EQUATIONS

In general, distributed networks in the presence of nonlinear elements can be expressed as

$$\begin{aligned}
 C_\phi \dot{\mathbf{x}}_\phi(t) + G_\phi \mathbf{x}_\phi(t) + \sum_{k=1}^{N_t} \mathbf{D}_k \mathbf{i}_k(t) + \mathbf{f}_\phi(\mathbf{x}_\phi(t)) \\
 = \mathbf{b}_\phi(t) \\
 \mathbf{I}_k(s) = \mathbf{Y}_k(s) \mathbf{V}_k(s)
 \end{aligned} \tag{1}$$

Manuscript received March 30, 2001; revised August 24, 2001.

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Publisher Item Identifier S 0018-9480(01)10466-7.

where

- $\mathbf{x}_\phi(t)$ is a vector, which include node voltages appended by independent and dependent voltage source currents, inductor currents, nonlinear capacitor charge, and nonlinear inductor flux waveform. \mathbf{G}_ϕ and \mathbf{C}_ϕ are constant matrices describing the lumped memoryless and memory elements of the network, respectively. $\mathbf{b}_\phi(t)$ is a vector with entries determined by the independent voltage and current sources. $\mathbf{f}_\phi(\mathbf{x}_\phi(t))$ is a vector describing the nonlinear elements.
- $\mathbf{D}_k = [d_{i,j} \in \{0, 1\}]$ is a selector matrix that maps $\mathbf{i}_k(t)$, the vector of terminal currents entering the interconnect subnetwork k , into the node space of the network, where $i \in \{1, \dots, n\}$, $j \in \{1, \dots, 2m_k\}$ and m_k is the number of coupled signal conductors in subnetwork k . N_t is the number of distributed structures. $\mathbf{Y}_k(s)$ is the admittance parameters of interconnect subnetwork k in the Laplace domain. \mathbf{I}_k and \mathbf{V}_k represent the Laplace terminal voltages and currents of interconnect k .

The distributed elements in (1) do not have a direct representation in the time domain, leading to mixed frequency/time simulation difficulty. In order to overcome this problem, a closed-form MTL model based on a Padé matrix-rational approximation has recently been proposed [14]–[16]. This interconnect model is shown to be efficient, passive, and suitable for passive model reduction techniques based on congruent transformations. Using this *interconnect macromodel*, (1) can be expressed as

$$\mathbf{C}_\pi \frac{d}{dt} \mathbf{x}(t) + \mathbf{G}_\pi \mathbf{x}(t) + \mathbf{f}(\mathbf{x}(t)) = \mathbf{b}(t) \quad (2)$$

where the distributed elements are now explicitly embedded in the modified nodal admittance (MNA) [18] matrices of the overall network. The matrices of (2) are described in greater detail in Section IV, which describes the matrix-rational approximation-based algorithm.

III. SENSITIVITY ANALYSIS

In this section, the sensitivity analysis is derived for both linear and nonlinear distributed networks.

A. Sensitivity Analysis of Linear Circuits

If distributed networks are not in the presence of nonlinear elements, then (2) can be expressed in the frequency domain as

$$(s\mathbf{C}_\pi + \mathbf{G}_\pi)\mathbf{X} = \mathbf{B} \quad (3)$$

where \mathbf{X} is the Laplace transform of $\mathbf{x}(t)$ and \mathbf{B} is the Laplace transform of $\mathbf{b}(t)$. An efficient method to obtain the sensitivities of the network is the adjoint technique [18], [19]. Let Φ be the output variable of interest, defined as

$$\Phi = \mathbf{z}^t \mathbf{X} \quad (4)$$

where \mathbf{z} is a selector vector with unity entry corresponding to the output variable and the superscript t denotes the transpose of a matrix. Differentiating (3) and (4) with respect to a circuit parameter λ and combining the two relations yields

$$\frac{\partial \Phi}{\partial \lambda} = -\mathbf{X}_a^t \left(s \frac{\partial \mathbf{C}_\pi}{\partial \lambda} + \frac{\partial \mathbf{G}_\pi}{\partial \lambda} \right) \mathbf{X} \quad (5)$$

where \mathbf{X}_a is the solution of the adjoint network and is defined as

$$(s\mathbf{C}_\pi + \mathbf{G}_\pi)^t \mathbf{X}_a = \mathbf{z}. \quad (6)$$

From (5), the sensitivity of an output variable with respect to a circuit parameter depends on \mathbf{X} , \mathbf{X}_a , $\partial \mathbf{C}_\pi / \partial \lambda$, and $\partial \mathbf{G}_\pi / \partial \lambda$. The values of \mathbf{X} and \mathbf{X}_a are obtained from the solution of the network and adjoint equations given by (3) and (6), respectively. For the case where λ is an interconnect parameter, the matrices $\partial \mathbf{C}_\pi / \partial \lambda$ and $\partial \mathbf{G}_\pi / \partial \lambda$ are derived from the equations of the interconnect model. How to obtain these matrices from the matrix-rational approximation-based algorithm is described in Section IV.

B. Sensitivity Analysis With Nonlinear Terminations

The sensitivity of the nonlinear network with respect to a circuit parameter λ is obtained by differentiating (2) with λ as

$$\mathbf{C}_\pi \frac{d}{dt} \mathbf{z}(t) + \left(\mathbf{G}_\pi + \frac{\partial \mathbf{f}(\mathbf{x}(t))}{\partial \mathbf{x}(t)} \right) \mathbf{z}(t) + \mathbf{E}(t) = \mathbf{0} \quad (7)$$

where

$$\begin{aligned} \mathbf{E}(t) &= \frac{\partial \mathbf{C}_\pi}{\partial \lambda} \frac{d}{dt} \mathbf{x}(t) + \frac{\partial \mathbf{G}_\pi}{\partial \lambda} \mathbf{x}(t) + \frac{\partial \mathbf{f}(\mathbf{x}(t))}{\partial \lambda} \\ \mathbf{z} &= \frac{\partial \mathbf{x}(t)}{\partial \lambda}. \end{aligned} \quad (8)$$

The solution of (2) and (7) can be obtained by converting them to difference equations using integration formulae such as backward Euler or trapezoidal rule (TR) [18]. For example, if TR is used, (2) and (7) become

$$\begin{aligned} \mathbf{A} \mathbf{X}_{n+1} + \frac{\mathbf{f}(\mathbf{X}_{n+1})}{2} \\ = \mathbf{B} \mathbf{X}_n - \frac{\mathbf{f}(\mathbf{X}_n)}{2} + \frac{(\mathbf{b}_{n+1} + \mathbf{b}_n)}{2} \end{aligned} \quad (9)$$

$$\begin{aligned} \left(\mathbf{A} + \frac{\partial \mathbf{f}(\mathbf{X}_{n+1})}{2 \partial \mathbf{X}_{n+1}} \right) \frac{\partial \mathbf{X}_{n+1}}{\partial \lambda} \\ = -\frac{\partial \mathbf{A}}{\partial \lambda} \mathbf{X}_{n+1} + \mathbf{B} \frac{\partial \mathbf{X}_n}{\partial \lambda} + \frac{\partial \mathbf{B}}{\partial \lambda} \mathbf{X}_n - \frac{\partial \mathbf{f}(\mathbf{X}_n)}{2 \partial \mathbf{X}_n} \frac{\partial \mathbf{X}_n}{\partial \lambda} \\ - \frac{1}{2} \left(\frac{\partial \mathbf{f}(\mathbf{X}_{n+1})}{\partial \lambda} + \frac{\partial \mathbf{f}(\mathbf{X}_n)}{\partial \lambda} \right) \end{aligned} \quad (10)$$

where

$$\mathbf{A} = \frac{\mathbf{C}_\pi}{\Delta t} + \frac{\mathbf{G}_\pi}{2} \quad \mathbf{B} = \frac{\mathbf{C}_\pi}{\Delta t} - \frac{\mathbf{G}_\pi}{2} \quad (11)$$

and $\mathbf{X}_n = \mathbf{X}(t_n)$. The variable Δt is the time step between the time interval t_n to t_{n+1} .

Equations (9) and (10) represent the solution of the original and sensitivity networks as described by (2) and (7). The coefficients on the right side of (10) are all known from the solution of (9). The variables $\partial \mathbf{f}(\mathbf{X}_{n+1}) / \partial \mathbf{X}_{n+1}$ and $\partial \mathbf{f}(\mathbf{X}_n) / \partial \mathbf{X}_n$ are the Jacobian matrices which can be obtained by solving (9). The matrices $\partial \mathbf{A} / \partial \lambda$ and $\partial \mathbf{B} / \partial \lambda$ are derived from $\partial \mathbf{C}_\pi / \partial \lambda$ and $\partial \mathbf{G}_\pi / \partial \lambda$. For the case when λ is an interconnect parameter, $\partial \mathbf{C}_\pi / \partial \lambda$ and $\partial \mathbf{G}_\pi / \partial \lambda$ are derived from the equations of the interconnect model. How to obtain these matrices from the matrix-rational approximation-based algorithm will be described in Section IV.

C. Sensitivity With Respect to Physical Interconnect Parameters

When studying the sensitivity of distributed networks, the design parameters of interconnects are usually required with respect to physical parameters (such as width and spacing of conductors). The sensitivities of electrical parameters are often intermediate steps to the calculation of sensitivities of physical parameters. In the case where λ represents a physical parameter of an interconnect, the sensitivity of the output nodes can be obtained as follows:

$$\frac{\partial \mathbf{x}}{\partial \lambda} = \sum_{i=1}^{m_k} \sum_{j=1}^{m_k} \left(\frac{\partial \mathbf{x}}{\partial R_{i,j}} \frac{\partial R_{i,j}}{\partial \lambda} + \frac{\partial \mathbf{x}}{\partial L_{i,j}} \frac{\partial L_{i,j}}{\partial \lambda} + \frac{\partial \mathbf{x}}{\partial G_{i,j}} \frac{\partial G_{i,j}}{\partial \lambda} + \frac{\partial \mathbf{x}}{\partial C_{i,j}} \frac{\partial C_{i,j}}{\partial \lambda} \right) \quad (12)$$

where $R_{i,j}$, $L_{i,j}$, $G_{i,j}$, and $C_{i,j}$ are the per-unit-length parameters. The subscripts i and j are matrix indexes.

IV. SENSITIVITY OF THE CLOSED-FORM MATRIX-RATIONAL APPROXIMATION-BASED MACROMODEL

It is clear from Section III, to calculate network sensitivities with respect to any interconnect parameter, the MNA derivatives with respect to the interconnect parameters are required. This section reviews the closed-form matrix-rational approximation-based MTL macromodel, which forms the basis of the proposed algorithm. From this discussion, the MNA derivatives with respect to the interconnect parameters (i.e., $\partial \mathbf{C}_\pi / \partial \lambda$ and $\partial \mathbf{G}_\pi / \partial \lambda$) are derived.

A. Review of the Closed-Form Matrix-Rational Approximation-Based Macromodel

Consider a $m + 1$ conductor transmission line (m signal conductors with one reference line) described by Telegrapher's equations

$$\begin{aligned} \frac{\partial}{\partial x} \mathbf{v}(x, t) &= -\mathbf{R}\mathbf{i}(x, t) - \mathbf{L} \frac{\partial}{\partial t} \mathbf{i}(x, t) \\ \frac{\partial}{\partial x} \mathbf{i}(x, t) &= -\mathbf{G}\mathbf{v}(x, t) - \mathbf{C} \frac{\partial}{\partial t} \mathbf{v}(x, t) \end{aligned} \quad (13)$$

where \mathbf{R} , \mathbf{L} , \mathbf{C} , and $\mathbf{G} \in \mathbb{R}^{m \times m}$ are the per-unit-length parameter matrices. The coefficients $\mathbf{v}(x, t)$ and $\mathbf{i}(x, t) \in \mathbb{R}^m$ represent the voltage and current vectors, as a function of position x and time t . Equation (13) can be written in the Laplace domain using the exponential function as

$$\begin{bmatrix} \mathbf{V}(d, s) \\ \mathbf{I}(d, s) \end{bmatrix} = e^{\mathbf{Z}d} \begin{bmatrix} \mathbf{V}(0, s) \\ \mathbf{I}(0, s) \end{bmatrix} \quad (14)$$

where

$$\mathbf{Z} = \begin{bmatrix} 0 & -\mathbf{a}(s) \\ -\mathbf{b}(s) & 0 \end{bmatrix} d; \quad \mathbf{a}(s) = \mathbf{R} + s\mathbf{L}; \quad \mathbf{b}(s) = \mathbf{G} + s\mathbf{C} \quad (15)$$

and d is the length of the line. The exponential matrix $e^{\mathbf{Z}}$ can be written as

$$e^{\mathbf{Z}} \approx [\mathbf{P}_N(\mathbf{Z})]^{-1} \mathbf{Q}_N(\mathbf{Z}) \quad (16)$$

where $\mathbf{P}_N(\mathbf{Z})$ and $\mathbf{Q}_N(\mathbf{Z})$ are **polynomial matrices** that can be expressed in terms of a closed-form matrix-rational function [14], [15]. Once $e^{\mathbf{Z}}$ is expressed as a rational function, it can be converted to ordinary differential equations. One way to obtain

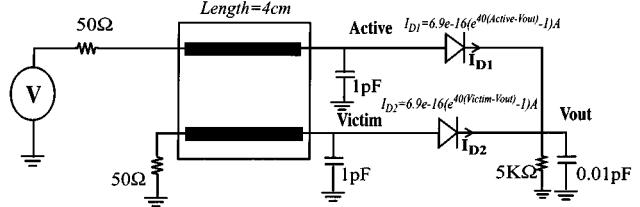
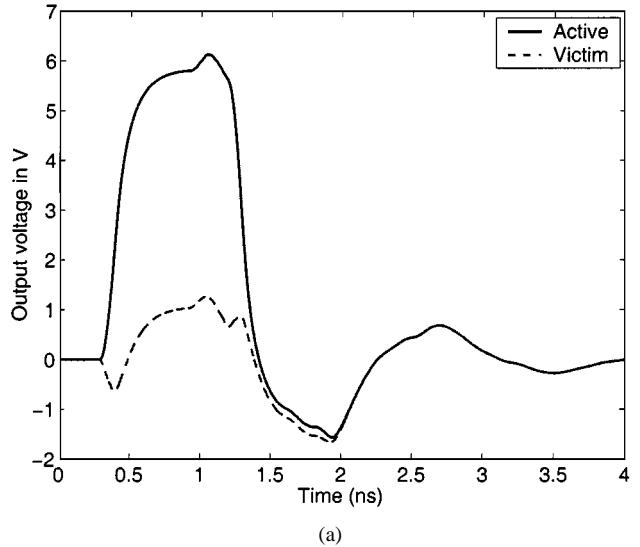
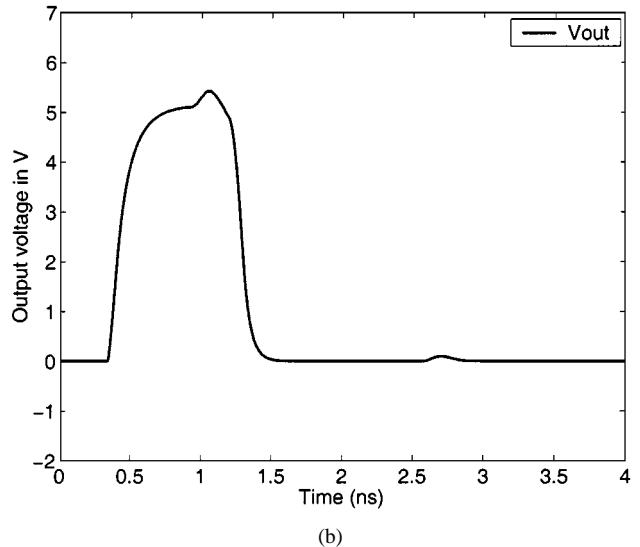


Fig. 1. Coupled interconnect system with nonlinear termination.



(a)



(b)

Fig. 2. Output transient of nonlinear circuit. (a) Response of active and victim line. (b) Response of V_{out} .

the differential equations is to express (16) in terms of subsections formed by the poles and zeros of the rational approximation. In this form, the macromodel can be represented in terms of lumped components [15].

Using the proposed macromodel, the nonlinear distributed network described by (1) is converted into the form of (2) where

$$\begin{aligned} \mathbf{G}_\pi &= \mathbf{G}_a + \sum_{k=1}^{N_t} \sum_i (\psi_i^k)^t \mathbf{G}_i^k \psi_i^k \\ \mathbf{C}_\pi &= \mathbf{C}_a + \sum_{k=1}^{N_t} \sum_i (\psi_i^k)^t \mathbf{C}_i^k \psi_i^k. \end{aligned} \quad (17)$$

Here the matrices \mathbf{G}_a , \mathbf{C}_a , $\mathbf{f}(\mathbf{x}(t))$, and $\mathbf{b}(t)$ are obtained from \mathbf{G}_ϕ , \mathbf{C}_ϕ , $\mathbf{f}_\phi(\mathbf{x}_\phi(t))$, and $\mathbf{b}_\phi(t)$ by appending them by rows (and/or) columns that contain zeros to account for the extra variables required for the stamp of the transmission line. Thus, \mathbf{G}_a , \mathbf{C}_a , $\mathbf{b}(t)$, and $\mathbf{f}(\mathbf{x}(t))$ can be expressed in the following block form:

$$\begin{aligned}\mathbf{G}_a &= \begin{bmatrix} \mathbf{G}_\phi & \mathbf{0} \\ \mathbf{0} & \mathbf{0} \end{bmatrix} \\ \mathbf{C}_a &= \begin{bmatrix} \mathbf{C}_\phi & \mathbf{0} \\ \mathbf{0} & \mathbf{0} \end{bmatrix} \\ \mathbf{f}(\mathbf{x}(t)) &= \begin{bmatrix} \mathbf{f}_\phi(\mathbf{x}_\phi(t)) \\ \mathbf{0} \end{bmatrix} \\ \mathbf{b}(t) &= \begin{bmatrix} \mathbf{b}_\phi(t) \\ \mathbf{0} \end{bmatrix}.\end{aligned}\quad (18)$$

The indexes i and k represent the i th subsection of the k th interconnect. The subsections of each interconnect are obtained from the poles and zeros of the Padé rational model of the exponential function. In the case of real pole-zero subsections, they can be expressed as (19a) and (19b), shown at the bottom of this page. For the complex pole-zero subsections, the matrices are expressed as (20a) and (20b), shown at the bottom of the following page.

The matrices ψ_i^k are selector matrices that map the block stamps \mathbf{G}_i^k and \mathbf{C}_i^k to the rest of the network equations. The matrices \mathbf{R}_k , \mathbf{L}_k , \mathbf{C}_k , and \mathbf{G}_k are the per-unit-length parameters of the k th MTL, d_k is the length of the k th MTL, \mathbf{U} is the unity matrix, and the variables $a_{i,k}$, $x_{i,k}$, and $\rho_{i,k}^2$ are predetermined constants given by the Padé approximation and are described in greater detail in [14] and [15].

It should be noted that the MNA matrices described by (19) and (20) are obtained analytically in terms of per-unit-length parameters and predetermined constants given by the matrix-rational approximation. An error criteria for selecting the order of the matrix-rational approximation is described in [14] and [15]. Next, the calculation of $\partial\mathbf{C}_\pi/\partial\lambda$ and $\partial\mathbf{G}_\pi/\partial\lambda$ is derived using the proposed MTL model.

B. Calculation of $\partial\mathbf{C}_\pi/\partial\lambda$ and $\partial\mathbf{G}_\pi/\partial\lambda$

To calculate the sensitivity of the network with respect to an interconnect parameter, the matrices $\partial\mathbf{C}_\pi/\partial\lambda$ and $\partial\mathbf{G}_\pi/\partial\lambda$ are required. The calculation of $\partial\mathbf{C}_\pi/\partial\lambda$ and $\partial\mathbf{G}_\pi/\partial\lambda$ depends on the interconnect model being used. For the case of matrix-rational approximation-based model, the matrices are obtained as follows. Let λ_k be an interconnect parameter of the k th interconnect. Differentiating the matrices of (17) with respect to λ_k yields

$$\begin{aligned}\frac{\partial\mathbf{G}_\pi}{\partial\lambda_k} &= \sum_i (\psi_i^k)^t \frac{\partial\mathbf{G}_i^k}{\partial\lambda_k} \psi_i^k \\ \frac{\partial\mathbf{C}_\pi}{\partial\lambda_k} &= \sum_i (\psi_i^k)^t \frac{\partial\mathbf{C}_i^k}{\partial\lambda_k} \psi_i^k.\end{aligned}\quad (21)$$

The matrices $\partial\mathbf{C}_i^k/\partial\lambda_k$ and $\partial\mathbf{G}_i^k/\partial\lambda_k$ are computed analytically in terms of per-unit-length parameters and predetermined constants given by the matrix-rational approximation. This makes the formulation of sensitivity networks extremely simple when compared to other MTL models. As an example, differentiating with respect to the conductance of the k th interconnect for the complex pole-zero subsection yields $\partial\mathbf{C}_i^k/\partial G_k = \mathbf{0}$ and

$$\frac{\partial\mathbf{G}_i^k}{\partial G_k} = \begin{bmatrix} \frac{d_k}{4x_{i,k}} & 0 & 0 & \frac{-d_k}{4x_{i,k}} & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & \frac{x_{i,k}d_k}{\rho_{i,k}^2} & \frac{x_{i,k}d_k}{\rho_{i,k}^2} & 0 & 0 & 0 \\ \frac{-d_k}{4x_{i,k}} & 0 & \frac{x_{i,k}d_k}{\rho_{i,k}^2} & \left(\frac{x_{i,k}d_k}{\rho_{i,k}^2} + \frac{d_k}{4x_{i,k}} \right) & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 \end{bmatrix}\quad (22)$$

for a single conductor line (with reference ground).

$$\mathbf{G}_i^k = \begin{bmatrix} \frac{d_k}{2a_{i,k}} \mathbf{G}_k & \mathbf{0} & \frac{d_k}{2a_{i,k}} \mathbf{G}_k & \mathbf{U} \\ \mathbf{0} & \frac{a_{i,k}}{2d_k} \mathbf{R}_k^{-1} & \frac{-a_{i,k}}{2d_k} \mathbf{R}_k^{-1} & -\mathbf{U} \\ \frac{d_k}{2a_{i,k}} \mathbf{G}_k & \frac{-a_{i,k}}{2d_k} \mathbf{R}_k^{-1} & \left(\frac{a_{i,k}}{2d_k} \mathbf{R}_k^{-1} + \frac{d_k}{2a_{i,k}} \mathbf{G}_k \right) & \mathbf{0} \\ -\mathbf{U} & \mathbf{U} & 0 & 0 \end{bmatrix}\quad (19a)$$

$$\mathbf{C}_i^k = \begin{bmatrix} \frac{d_k}{2a_{i,k}} \mathbf{C}_k & \mathbf{0} & \frac{d_k}{2a_{i,k}} \mathbf{C}_k & \mathbf{0} \\ \mathbf{0} & \mathbf{0} & \mathbf{0} & \mathbf{0} \\ \frac{d_k}{2a_{i,k}} \mathbf{C}_k & \mathbf{0} & \frac{d_k}{2a_{i,k}} \mathbf{C}_k & \mathbf{0} \\ \mathbf{0} & \mathbf{0} & \mathbf{0} & \frac{2d_k}{a_{i,k}} \mathbf{L}_k \end{bmatrix}\quad (19b)$$

V. COMPUTATION RESULTS

Example 1: A coupled interconnect system with nonlinear termination is shown in Fig. 1. The length of the line is 4 cm and the per-unit-length parameters are

$$R = \begin{bmatrix} 0.14 & 0 \\ 0 & 0.14 \end{bmatrix} \Omega/\text{cm}$$

$$L = \begin{bmatrix} 6.3 & 2.9 \\ 2.9 & 6.3 \end{bmatrix} \text{nH/cm}$$

$$C = \begin{bmatrix} 1.1 & -0.45 \\ -0.45 & 1.1 \end{bmatrix} \text{pF/cm.}$$

Fig. 2 shows transient responses of the far-end voltages corresponding to a 5-V input pulse with rise/fall times 0.1 ns and a pulselength of 0.8 ns. The sensitivities with respect to C_{11} for the active, victim, and V_{out} nodes are shown in Fig. 3. The results of the proposed method are compared with the perturbation of the lumped segment model [8] (referred to as SPICE Perturbation). Both the proposed method and the perturbation results are in good agreement.

It is to be noted that using the proposed method provides the following advantages.

- 1) Using the closed-form matrix-rational approximation-based macromodel provides significant CPU advantages compared to the lumped segmentation model [15]. For this example, the total size of the MNA matrices was 93×93 using the proposed method while the size of the lumped model was 407×407 .
- 2) Perturbation-based techniques can lead to inaccurate results (depending on the magnitude of the perturbation).
- 3) In addition, the nonlinear differential equations representing the perturbed network must be solved separately for every parameter of interest. However, in the proposed approach, the sensitivity information with respect to all the parameters can be essentially obtained from the solution of the original network.

Table I shows a comparison of savings in the main computational cost (in terms of total number of LU decompositions) using the proposed approach versus the perturbation approach, for the above example.

Example 2: In this example, interconnect physical parameters are optimized to obtain desired circuit performance. A circuit with three interconnect subnetworks is shown in Fig. 4. Each subnetwork consists of two coupled transmission lines and a ground plane, as shown Fig. 5. A 5-V step response with a

$$G_i^k = \begin{bmatrix} \left(\frac{x_{i,k}}{d_k} + \frac{\rho_{i,k}^2}{4x_{i,k}d_k} \right) R_k^{-1} + \frac{d_k}{4x_{i,k}} G_k & \frac{-x_{i,k}}{d_k} R_k^{-1} & 0 & \frac{-d_k}{4x_{i,k}} G_k & \frac{-\rho_{i,k}^2}{4x_{i,k}d_k} R_k^{-1} & 0 & 0 \\ \frac{-x_{i,k}}{d_k} R_k^{-1} & \frac{x_{i,k}}{d_k} R_k^{-1} & 0 & 0 & 0 & U & 0 \\ 0 & 0 & \frac{x_{i,k}d_k}{\rho_{i,k}^2} G_k & \frac{x_{i,k}d_k}{\rho_{i,k}^2} G_k & 0 & -U & 0 \\ \frac{-d_k}{4x_{i,k}} G_k & 0 & \frac{x_{i,k}d_k}{\rho_{i,k}^2} G_k & \left(\frac{x_{i,k}d_k}{\rho_{i,k}^2} + \frac{d_k}{4x_{i,k}} \right) G_k & 0 & 0 & U \\ \frac{-\rho_{i,k}^2}{4x_{i,k}d_k} R_k^{-1} & 0 & 0 & 0 & \frac{\rho_{i,k}^2}{4x_{i,k}d_k} R_k^{-1} & 0 & -U \\ 0 & -U & U & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & -U & U & 0 & 0 \end{bmatrix} \quad (20a)$$

$$C_i^k = \begin{bmatrix} \frac{d_k}{4x_{i,k}} C_k & 0 & 0 & \frac{-d_k}{4x_{i,k}} C_k & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & \frac{x_{i,k}d_k}{\rho_{i,k}^2} C_k & \frac{x_{i,k}d_k}{\rho_{i,k}^2} C_k & 0 & 0 & 0 \\ \frac{-d_k}{4x_{i,k}} C_k & 0 & \frac{x_{i,k}d_k}{\rho_{i,k}^2} C_k & \left(\frac{x_{i,k}d_k}{\rho_{i,k}^2} + \frac{d_k}{4x_{i,k}} \right) C_k & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & \frac{d_k}{x_{i,k}} L_k & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & \frac{4x_{i,k}d_k}{\rho_{i,k}^2} L_k \end{bmatrix} \quad (20b)$$

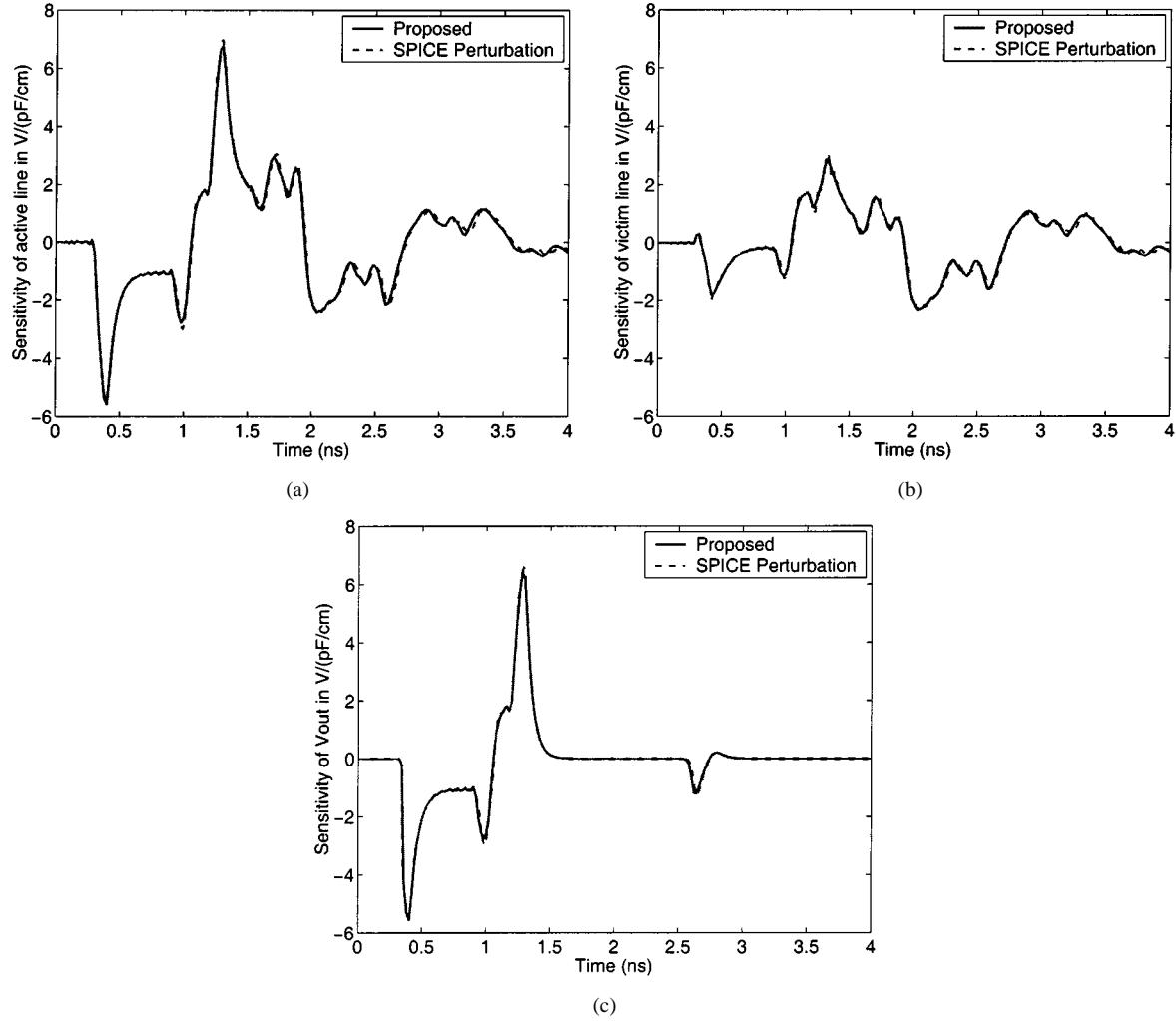
Fig. 3. Sensitivity w.r.t. C_{11} for: (a) the active line, (b) the victim line, and (c) V_{out} .

TABLE I
COMPUTATIONAL COMPLEXITY: PROPOSED VERSUS PERTURBATION

# of parameters	Perturbation (# LU decompositions)	Proposed (# LU decompositions)
10	17501	1601

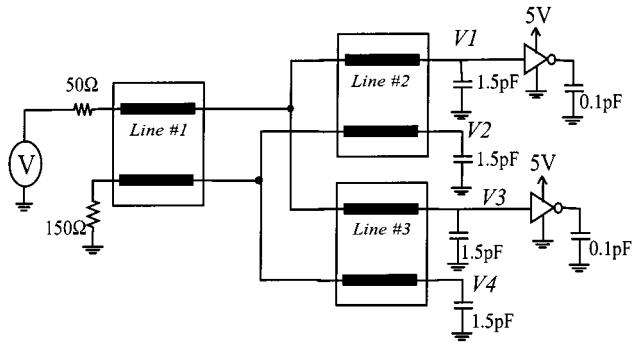


Fig. 4. Three coupled interconnects with nonlinear terminations.

time rise of 1 ns is the input signal of the circuit. The response of interest are nodes labeled V_1 , V_2 , V_3 , and V_4 . The responses before optimization are plotted in Fig. 6. The delays of V_1 and

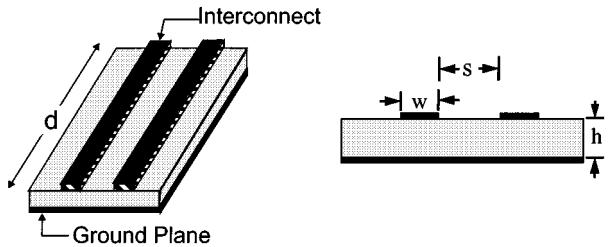


Fig. 5. Physical/geometrical parameters in a two-conductor transmission line.

V_3 are to be reduced to 5.5 and 5.0 ns, respectively, based on a threshold voltage of 3.0 V. In addition, it is desired to maintain the voltages of V_1 and V_3 greater than 4 V after 6.5 and 6.0 ns, respectively. A 0.4-V upper specification is imposed on the magnitude of V_2 and V_4 . The error functions for V_1 and V_3 become

$$\begin{aligned}
 e_1 &= [V_1(\phi, 5.5 \text{ ns}) - 3 \text{ V}] \\
 e_2 &= -[V_1(\phi, 5.5 \text{ ns}) - 3 \text{ V}] \\
 e_3 &= [V_3(\phi, 5.0 \text{ ns}) - 3 \text{ V}] \\
 e_4 &= -[V_3(\phi, 5.0 \text{ ns}) - 3 \text{ V}] \\
 e_{i+4} &= -[V_1(\phi, t_i) - 4 \text{ V}] \\
 e_{j+20} &= -[V_3(\phi, t_j) - 4 \text{ V}]
 \end{aligned}$$

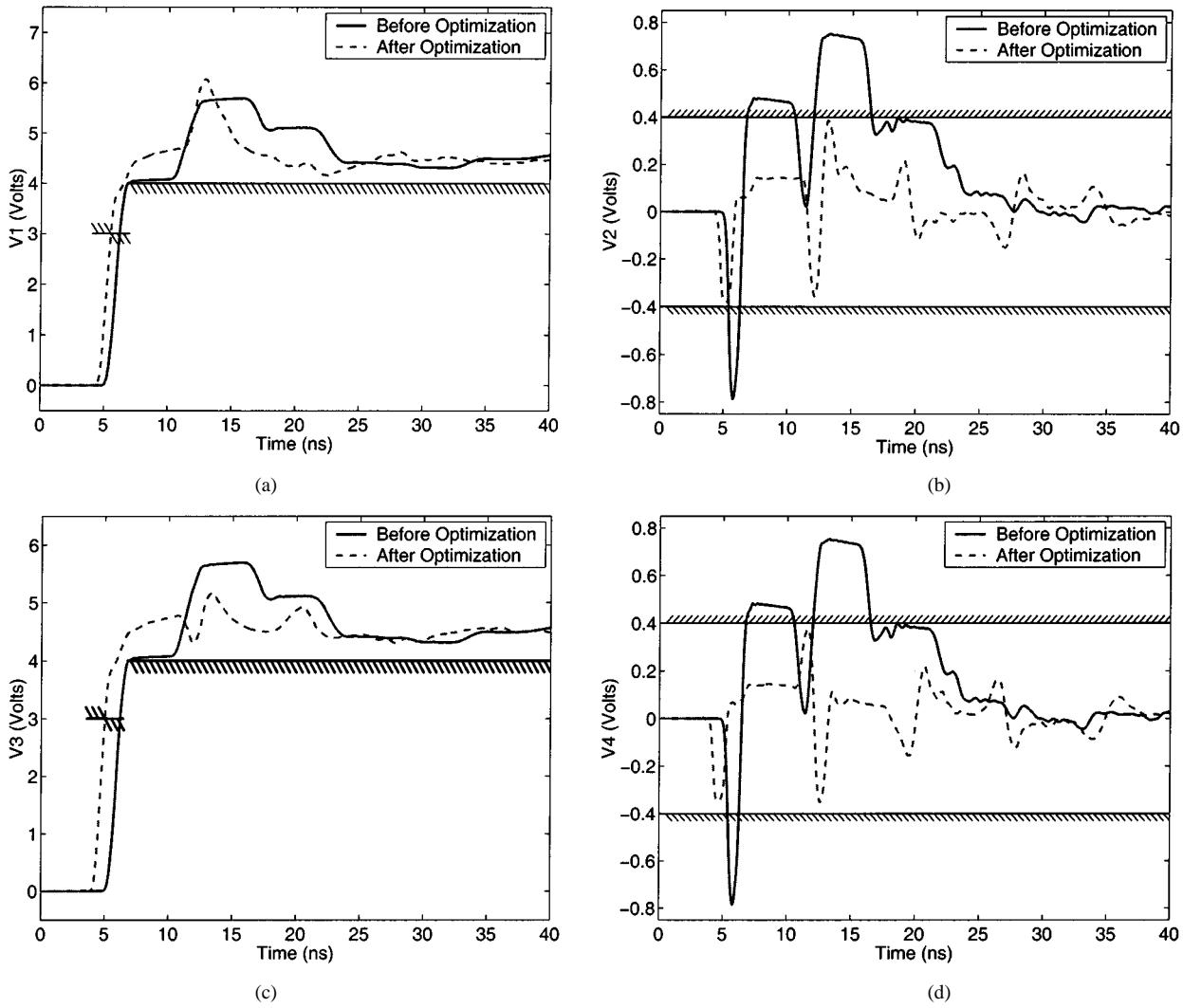


Fig. 6. Signal response of nonlinear circuit before and after optimization (example 3). (a) Signal response V_1 . (b) Signal response V_2 . (c) Signal response V_3 . (d) Signal response V_4 .

where $i = [1, 2, \dots, 16]$, $j = [1, 2, \dots, 16]$, t_i and t_j are equidistant time points between the interval [6.5 ns, 40.0 ns] and [6.0 ns, 40.0 ns], respectively. A weight factor of 10 is used to make the error functions of V_2 and V_4 comparable in scale to those of V_1 and V_3 . The error functions e_{37} to e_{100} for V_2 and V_4 become

$$\begin{aligned} e_{k+36} &= 10[V_2(\phi, t_k) - 0.4 \text{ V}] \\ e_{k+52} &= -10[V_2(\phi, t_k) + 0.4 \text{ V}] \\ e_{k+68} &= 10[V_4(\phi, t_k) - 0.4 \text{ V}] \\ e_{k+84} &= -10[V_4(\phi, t_k) + 0.4 \text{ V}] \end{aligned}$$

where $k = [1, 2, \dots, 16]$ and t_k are equidistant time points between the interval [0.0 ns, 40.0 ns]. As seen from Fig. 6, all specifications are violated before optimization.

The design variables ϕ include linewidth w , spacing between conductors s , circuit board thickness h , and lengths of multiconductor lines d_1 , d_2 , and d_3 . Parameters w , s , and h are the same for all three transmission lines. Design constraints of the circuit require that the total length of the three transmission lines be 1.35 m and the total width of the two conductors plus the spacing between them be fixed at 2.5 mm. In addition, the

width of the line has to be equal to or greater than 0.1 mm and the allowable range of the circuit board thickness is between 0.5–2.5 mm. The constraints on the design variables are

$$\begin{aligned} g_1(\phi) &= 2w + s - 2.5 \text{ mm} \\ g_2(\phi) &= d_1 + d_2 + d_3 - 1.35 \text{ m} \\ w &\geq 0.1 \text{ mm} \\ 0.5 \text{ mm} &\leq h \leq 2.5 \text{ mm.} \end{aligned}$$

The relative dielectric constants of the circuit board is 4.5. The initial values of the variables are $w = 0.5$ mm, $s = 1.5$ mm, $h = 2.0$ mm, $d_1 = 0.45$ m, $d_2 = 0.45$ m, and $d_3 = 0.45$ m. The per-unit-length parameters are computed from the physical description of the transmission line by using an interconnect modeling tool [20]. The results obtained are used to formulate a lookup table. Intermediate data points are interpolated using Stirling's formula [21]. This results in relatively more accurate per-unit-length parameters during the optimization process.

The design variables after optimization are $w = 0.149$ mm, $s = 2.202$ mm, $h = 0.998$ mm, $d_1 = 0.132$ m, $d_2 = 0.651$ m, and $d_3 = 0.567$ m. The optimized circuit responses are juxtaposed against those before optimization in Fig. 6. The

new design variables meet all the circuit specifications without violating any design constraint.

VI. CONCLUSION

A new approach for sensitivity analysis of lossy multiconductor transmission lines in the presence of nonlinear terminations is described. Sensitivity information is derived from the recently developed closed-form matrix-rational approximation-based transmission-line model. The method enables sensitivity analysis of interconnect structures with respect to both electrical and physical parameters, while providing significant computational cost advantages.

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